

International Workshop on Nanodevice Technologies 2024 (IWNT 2024) - Frontline of semiconductor technologies and industrial activities -

November 15th , 2024, Hiroshima, Japan

Venue: Higashi Hiroshima Arts & Culture Hall Kurara
Organizer: Research Institute for Semiconductor
Engineering (RISE), Hiroshima University

Invited Speakers(Tentative):

Muhammad Alam, Purdue University, USA
"The Emerging Frontier of More-Moore and More-than-Moore Applications"

Kurtis Cantley, Boise State University, USA
"Semiconductor Materials and Devices for Spiking Neuromorphic Architectures"

Takashi Hayakawa, Tokyo Electron, Japan
"Challenge & Opportunity for Further Scaling Scenario"

Takayuki Iwaki, Micron Memory Japan, Japan
"DRAM Scaling Challenges and Innovations for AI Era"

Tetsuo Sasaki, Shizuoka University, Japan
"Terahertz Laser Spectrometer and its Applications Mainly for Pharmaceutical Industry"

Yasuhiko Ishikawa, Toyohashi U. Tech., Japan
"Ge-based photodetectors and electro-absorption modulators in Si Photonics"

Koichi Kato, Hiroshima University, Japan
"Micropatterned Chips for Stem Cell Characterization"

Kenya Shimada, Hiroshima University, Japan
"Surface/interface Studies using Synchrotron Radiation"

Deadlines

Poster Application:
11th October, 2024

Registration:
25th October, 2024

Organizers:

-Research Institute for Semiconductor Engineering (RISE), Hiroshima University

-Setouchi Semiconductor Consortium

Venue:

Higashi Hiroshima Arts & Culture Hall Kurara(Higashi-Hiroshima, Hiroshima, Japan)

Access:

JR Sanyo Main Line Saijo station, 4 min. walk



Application and Contact

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